

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	
KLIESNER ET AL.	)	
Serial No. 10/620,151	)	Atty. Docket No.:
	)	72206
Filing Date: 07/15/2003	)	
	)	Art Unit: 2611
Confirmation No. 8500	)	
	)	Examiner:
For: DIGITAL CLOCK RECOVERY CIRCUIT	)	K. C. TRAN
EMPLOYING FIXED CLOCK OSCILLATOR	)	
DRIVING FRACTIONAL DELAY LINE	)	
	)	

REQUEST FOR RECONSIDERATION UNDER 37 CFR § 1.116

**EFILED**

Commissioner for Patents

Sir:

In reply to the Examiner's Final Rejection dated August 16, 2007, Applicants submit the following remarks.

\*\*\*\*\*